

US009680041B2

## (12) United States Patent

Moslehi et al.

(54) THREE-DIMENSIONAL THIN-FILM SEMICONDUCTOR SUBSTRATE WITH THROUGH-HOLES AND METHODS OF MANUFACTURING

(71) Applicant: Solexel, Inc., Milpitas, CA (US)

(72) Inventors: **Mehrdad M. Moslehi**, Los Altos, CA (US); **David Xuan-Qi Wang**, Fremont,

CA (US)

(73) Assignee: Solexel, Inc., Milpitas, CA (US)

(\*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35

U.S.C. 154(b) by 0 days.

This patent is subject to a terminal dis-

claimer.

(21) Appl. No.: 14/578,161

(22) Filed: Dec. 19, 2014

(65) Prior Publication Data

US 2015/0303331 A1 Oct. 22, 2015

## Related U.S. Application Data

- (63) Continuation of application No. 13/962,291, filed on Aug. 8, 2013, now Pat. No. 8,916,772, which is a continuation of application No. 12/791,842, filed on Jun. 1, 2010, now Pat. No. 8,551,866.
- (60) Provisional application No. 61/182,635, filed on May 29, 2009, provisional application No. 61/228,068, filed on Jul. 23, 2009.

(10) Patent No.: US 9,680,041 B2

(45) **Date of Patent:** \*Jun. 13, 2017

(51) **Int. Cl.** H01L 31/00 (2006.01)(2006.01)H01L 31/0352 H01L 31/18 (2006.01)H01L 31/0468 (2014.01)H01L 27/142 (2014.01)H01L 31/0224 (2006.01)H01L 31/054 (2014.01)H01L 31/056 (2014.01)

(52) U.S. Cl.

CPC ...... H01L 31/03529 (2013.01); H01L 27/142 (2013.01); H01L 31/022458 (2013.01); H01L 31/035281 (2013.01); H01L 31/0468 (2014.12); H01L 31/1804 (2013.01); H01L 31/1892 (2013.01); H01L 31/054 (2014.12); H01L 31/056 (2014.12); Y02E 10/547 (2013.01); Y02P 70/521 (2015.11)

(58) Field of Classification Search

CPC ...... H01L 31/1892; H01L 31/0468; H01L 31/054; H01L 31/056; H01L 27/142; Y02E 10/547

See application file for complete search history.

Primary Examiner — Matthew Martin

## (57) ABSTRACT

A three-dimensional thin-film semiconductor substrate with selective through-holes is provided. The substrate having an inverted pyramidal structure comprising selectively formed through-holes positioned between the front and back lateral surface planes of the semiconductor substrate to form a partially transparent three-dimensional thin-film semiconductor substrate.

## 5 Claims, 33 Drawing Sheets

